

	Type	L #	Hits	Search Text	DBS	Time Stamp
1	BRS	L1	114	(processor with (state adj1 machine) with (control adj1 logic))	USPAT	2007/11/29 17:13
2	BRS	L2	76	1 and @ad<"20000614"	USPAT	2007/11/29 17:57
3	IS&R	L3	1	("20060101164") .PN.	US-PGPUB ; USPAT	2007/11/29 17:57
4	BRS	L5	1	3 and (state adj1 machine)	US-PGPUB ; USPAT	2007/11/29 17:58

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